~~S18~~	~~9~~	~~(light adj activated adj switch) and (pin adj diode)~~	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	~~OR~~	~~ON~~	~~2005/06/29 11:57~~
S24	1030	dmos and cmos	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/05 11:25
S25	840	dmos and cmos and well	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/05 14:09
S26	375	dmos and cmos and well and buried	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/05 19:41
S27	671	257/371.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/05 19:42
S28	471	257/357.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/05 19:42
S29	166	257/338.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/05 19:43
S30	915	257/341.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/05 19:44

S31	1111	257/350.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/06 20:06
S32	0	"5386135".pn. and (controller or (fixed adj voltage))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/06 20:06